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We have shown the implementation of the various buffers, the data flow and the control flow for a pipelined implementation of the MIPS architecture. Web Links / Supporting Materials Computer Organization and Design - The Hardware / Software Interface, David A. Patterson and John L. Hennessy, 4th.Edition, Morgan Kaufmann, Elsevier, 2009.

Pipelining - MIPS Implementation - Computer Architecture

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Computer architecture - Wikipedia

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