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 byte) ...

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8085 Microprocessor

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Alphabetical Order Sr.
No. Mnemonics,
Operand Opcode Bytes

1. ACI Data CE 2 2.
ADC A 8F 1 3. ADC B
88 1 4. ADC C 89 1 5.
ADC D 8A 1 6. ADC E
8B 1 7. ADC H 8C 1 8.
ADC L 8D 1 9. ADC M
8E 1 10. ADD A 87 1
11. ADD B 80 1 12.
ADD C 81 1 13. ...

**OPCODES TABLE OF
INTEL 8085 Opcodes
of Intel 8085 in ...**

8086 Instruction
Encoding-8 Addressing
Mode 00! Specifies R/M
Table 1 (with NO
displacement) 000
[BX+SI] 010 [BP+SP]
100 [SI] 110 Drc't Add
001 [BX+DI] 011
[BP+DI] 101 [DI] 111
[BX]! Note that the 110
case (direct
addressing) requires

that the instruction be
followed by two
address bytes There
are then two
possibilities: 1 Opcode
Addressing Mode

**Encoding of 8086
Instructions 8086
Instructions are ...**

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opcode-sheet-free 1/1
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The binary code
corresponding to this
instruction is 0111
1000 and its opcode is
78 H. ADD C. This
instruction has a task
to add the data present
in register C with the
accumulator (A) and
store the result in the
accumulator. The
binary code
corresponding to this

instruction is 10000001
and its opcode is 81 H.
OpCodes Table of 8085
Microprocessor

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Opcode Sheet -
Illustration. The bit
data of the specified
register pair are added
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Intel 8086 - Wikipedia

Thethat the takes only 1 microsecond to fetch an opcode and set or-

clear a port pin. It is substantially more flexible than a. There are two configurations of DP Some examples of how to use the iRAM

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