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### **Electronic Design Automation | ScienceDirect**

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In computer engineering, logic synthesis is a process by which an abstract specification of desired circuit behavior, typically at register transfer level, is turned into a design implementation in terms of logic gates, typically by a computer program called a synthesis tool. Common examples of this process include synthesis of designs specified in hardware description languages, including VHDL and Verilog. Some synthesis tools generate bitstreams for programmable logic devices such as PALs or F

### **Logic synthesis - Wikipedia**

Given an electronic system modeled at the electronic system level (ESL), EDA automates the design and test processes of verifying the correctness of the ESL design against the specifications of the electronic system, taking the ESL design through various synthesis and verification steps, and finally testing the manufactured electronic system to ensure that it meets the specifications and quality requirements of the electronic system.

### **Electronic Design Automation: Synthesis, Verification, and ...**

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