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1-2 Freescale

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asymmetric

multiprocessing (AMP),

in which each core

runs standalone, and

symmetric

multiprocessing (SMP),

in which the many

cores act as one through the operating system. Subsequent chapters focus on hardware, software architecture (such as

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Embedded Staff
Editor's Note: This article is an introduction to the Multicore Association (MCA) Multicore Programming Practices Guide developed by the members and available for download from the MCA web site. The Guide is a detailed set of best practices for employing an evolutionary approach

to multicore development.

Introduction to the Multicore Programming ... - Embedded.com

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Overview The NXP ® ASK (Applications Solutions Kit) Software package for the LS1043A processor turns the LS1043A into a packet processing beast, offering dozens of amazing NO-CPU-NEEDED packet transfers so that the FOUR incredible ARM A53 CPUs can be harnessed to make applications that thrill your customers.

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Mentor Embedded Supports NXP's Multicore QorIQ Processors

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This article is going to be an introduction to embedded Linux security.. Since this topic is quite extensive, I divided into two parts. In this first part, we will have a small introduction to security concepts and threat modeling and then focus on some mitigation techniques to improve the security of an embedded Linux device, including

secure boot, code/data encryption and secure key storage.

Introduction to Embedded Linux Security - part 1 - #

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The Multicore Association was founded in 2005. Multicore Association is a member-funded, non-profit, industry consortium focused on the creation of open standard APIs, specifications, and guidelines that allow system developers and programmers to more readily adopt multicore technology into their applications. The consortium provides a neutral forum for vendors and developers who are interested in, working with, and/or proliferating multicore-related products,

including processors, infrastructure

Multicore Association - Wikipedia

Fabricated with an advanced semiconductor process, the NXP i.MX 8M Mini applications processor provides the required mix of low power consumption and high performance heterogeneous multicore processing capability. Using NXP i.MX 8M Mini devices, developers can respond more effectively to emerging requirements for high performance computing in embedded systems designs needed in increasingly sophisticated smart products.

Multicore Processors

Provide High Performance | DigiKey

The CL-SOM-iMX7 FreeRTOS BSP is based on the NXP FreeRTOS BSP. The NXP BSP has some useful documents in the BSP archive, one of which provides a comprehensive documentation of FreeRTOS API. The NXP BSP can be downloaded here in the Board Support Packages section. Multicore development and coexisting with Linux

CL-SOM-iMX7: FreeRTOS: Introduction to NXP FreeRTOS ...

Multicore embedded systems are rapidly emerging. Hardware designers are packing more and more features into their design. Introducing heterogeneity in these systems, i.e., adding cores of varying types, provides opportunities to solve problems in different aspects. However, those designs present several challenges to embedded